Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**ANODE**

**.058 X .058”**

**.064”**

**.064”**

**For Zener operation, Cathode must be operated positive with respect to Anode.**

**Top Material: Al**

**Backside Material: Au**

**Bond Pad Size: .058” X .058”**

**Backside Potential: Cathode**

**Mask Ref: CZENR-O64**

**APPROVED BY: DK DIE SIZE .064” X .064” DATE: 11/10/21**

**MFG: MICROSEMI / CDI THICKNESS .010” P/N: 1N5352B**

**DG 10.1.2**

#### Rev B, 7/19/02